

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	"5881295".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 11:21
S2	2	"5245572".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/10 13:22
S3	4	"5950222".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:30
S4	667960	interrupt vector\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 15:35
S13	2	"20040153906".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 15:34
S14	4248	interrupt near2 vector\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 15:35
S15	408	S14 and "711"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 15:36
S16	33	S14 same convers\$4 same address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 15:52

S17	1895	S14 same register\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 15:53
S18	1036	S14 with register\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 15:52
S19	1124	S14 same register\$1 same address\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 15:53
S20	409	S14 with register\$1 with address\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 15:53
S21	59	S20 and "711"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 16:27
S28	10	("5293591" "5351216" "5812867").PN. OR ("5950222"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 10:18
S29	14	("5410711" "5539890" "5603038").PN. OR ("5881295"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 14:04
S30	6857	interrupt near2 program	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 14:06
S31	570	S30 same vector	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 14:48
S32	75	S31 and "711"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 14:07
S33	44	S30 same vector same first same second	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 14:55
S34	63	S30 same vector same (area or region)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 15:32

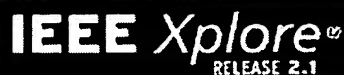
S35	3	S34 same first same second	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 14:55
S36	912	(convert\$3 or conversion) same ((vector with address) or (vector with interrupt))	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 15:34
S37	449	(convert\$3 or conversion) with ((vector with address) or (vector with interrupt))	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 15:38
S38	103	S36 same offset	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 15:34
S39	22	S36 same offset same register	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 15:34
S40	1267	vector with (interrupt or address or table) with add\$3	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 15:39
S41	111	S40 with offset	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 15:39
S42	25	S41 with register	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/09 16:20
S43	12	S42 and microcomputer	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/10 14:15
S44	10	(conversion near2 circuit) same vector near2 (interrupt or address)	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/09 16:23
S45	419	(conversion or convert\$3) same vector near2 (interrupt or address)	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/09 16:24
S46	123	(conversion or convert\$3) same vector near2 (interrupt or address) same register	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/09 16:24
S51	58	S46 and ("709"/\$.ccls. or "710"/\$. ccls. or "711"/\$.ccls. or "712"/\$. ccls. or "713"/\$.ccls. or "714"/\$. ccls.)	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/09 16:27
S52	0	system on chip	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 10:56
S53	1549	system-on-chip	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 10:35
S54	105	S53 and "711"/\$.ccls.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 11:02

S55	19	S54 and microcomputer	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 10:37
S56	104	S53 and microcomputer	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 10:46
S57	279	systems-on-a-chip	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 10:46
S58	7	S57 and microcomputer	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 10:56
S59	1	systems on a chip	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 10:56
S60	279	systems-on-a-chip	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 11:00
S61	7	S60 and microcomputer	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 11:02
S62	7	("5504903" "6026443" "6438678" "6467009" "6564329" "6598148" "RE38108").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/10 10:57
S63	1509	system-on-a-chip	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 11:01
S64	44	S63 and microcomputer	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 11:00
S65	281	(system-on-a-chip) and (advantage same (cost or speed))	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/11 15:34
S67	7	S65 and microcomputer	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 11:02
S68	8	S65 and "711"/\$.ccls.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 11:05
S69	350	(system-on-a-chip) and ((advantage or benefit) same (cost or speed))	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 11:05
S70	162	(system-on-a-chip) and (benefit same (cost or speed))	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 11:05
S71	4	S70 and "711"/\$.ccls.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/10 11:05

S72	3950	register with output with interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:31
S73	698	register with output with interrupt with address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/10 13:22
S74	78	register with output with interrupt with address with CPU	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/10 13:24
S75	259	register with output with interrupt with address with (CPU or central processing unit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/10 13:28
S76	61	S75 and microcomputer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/10 13:29
S79	216	register with access\$3 with interrupt with address with (CPU or central processing unit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/10 13:28
S80	42	S79 and microcomputer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/10 13:29
S81	4	(vector with offset with register\$1) same ((CPU or central processing unit) with access with address)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/10 14:17
S82	1845	710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:31

S83	3950	register with output with interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:31
S84	362	S83 and S82	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:31
S85	698	register with output with interrupt with address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:31
S86	90	S85 and S82	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:31
S87	259	register with output with interrupt with address with (CPU or central processing unit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:39
S88	31	S87 and S82	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:39
S89	3053	711/100-103.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:35
S90	7	S87 and S89	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:35

S91	1390	register with interrupt with address with (CPU or central processing unit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:39
S92	191	S91 and S82	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:40
S93	146	register with interrupt with address with (CPU or central processing unit) with vector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:39
S94	37	S93 and S82	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:49
S95	3	S93 and S89	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 14:49
S96	3	(system-on-a-chip) and (advantage same (cost or speed)) and (interrupt vector\$1)	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/11 15:34



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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

Select Article Information

- ☐ 1. **A platform for system-on-a-chip design prototyping**
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